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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/576,021	05/23/2000	Bahadir Erimli	95-311	4392	
20736 7	7590 07/15/2005		EXAMINER		
MANELLI DENISON & SELTER 2000 M STREET NW SUITE 700			JONES, PRENELL P		
	N. DC 20036-3307		ART UNIT	PAPER NUMBER	
	,		2667		
			D. MT 14. 11 TD 0514 4 1000	_	

DATE MAILED: 07/15/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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•		Applic	cation No.	Applicant(s)	0				
Office Action Summary		09/57	6,021	ERIMLI ET AL.					
		Exam	iner	Art Unit					
	•		II P. Jones	2667					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply									
THE - Exte after - If the - If NO - Failt Any	MAILING DATE OF THIS COMMUN ensions of time may be available under the provisions of time may be available under the provisions of SIX (6) MONTHS from the mailing date of this common of the proof of t	ICATION. s of 37 CFR 1.136(a). In n nunication. 30) days, a reply within the tatutory period will apply as y will, by statute, cause the	to event, however, may a statutory minimum of th nd will expire SIX (6) MC sapplication to become A	reply be timely filed irty (30) days will be considered timely. NTHS from the mailing date of this con ABANDONED (35 U.S.C. § 133).	nmunication.				
Status		•							
1)⊠	Responsive to communication(s) file	ed on <i>4/11/05</i> .							
2a)□	•	2b)⊠ This action	is non-final.						
3)									
,	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.								
Disposit	ion of Claims	•		•					
5)⊠ 6)⊠	<ul> <li>✓ Claim(s) 1-17 is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdrawn from consideration.</li> <li>✓ Claim(s) 12-17 is/are allowed.</li> <li>✓ Claim(s) 1 is/are rejected.</li> <li>✓ Claim(s) 2-11 is/are objected to.</li> <li>✓ Claim(s) are subject to restriction and/or election requirement.</li> </ul>								
Applicat	ion Papers								
9)□	The specification is objected to by th	e Examiner.							
· · · · ·	10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.								
·	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11)	The oath or declaration is objected to	o by the Examiner	. Note the attache	ed Office Action or form PTC	D-152.				
Priority (	under 35 U.S.C. § 119				•				
а)	Acknowledgment is made of a claim  All b) Some * c) None of:  1. Certified copies of the priority  2. Certified copies of the priority  3. Copies of the certified copies application from the Internation	documents have lead ocuments have lead of the priority document Bureau (PCT	been received. been received in uments have bee Rule 17.2(a)).	Application No n received in this National S	Stage				
Attachmen									
	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (F	PTO 048\		Summary (PTO-413) (s)/Mail Date					
3) 🔲 Infor	ce of Draftsperson's Patent Drawing Review (F mation Disclosure Statement(s) (PTO-1449 or er No(s)/Mail Date			Informal Patent Application (PTO-	152)				

Application/Control Number: 09/576,021 Page 2

Art Unit: 2667

## Response to Arguments

1. Applicant's arguments with respect to claim 1-17 are have been considered but are moot in view of the new ground(s) of rejection.

## Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).
- 4. Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over Aimoto in view of Kornaros et al.

Regarding claim 1, Aimoto discloses packet switching that utilizes prioritizing switching of data packets to an output port (Fig. 1, 3, Abstract, a network switch that has a priority

Application/Control Number: 09/576,021 Page 3

Art Unit: 2667

controller for prioritizing switching of packets into queue groups as the packets are read out to output port, col. 2, line 32-53), user-defined policy (Fig. 4, a contract between user and network provider/user-defined policy, wherein certain levels of service are guaranteed for the user, such as packet delay time, assigned bandwidth, packet discard rate and more, col. 6, line 47-64), based on a user-selected attribute of a packet (Fig. 5, priority defined in user mapping table, which includes transmission priority with respect to combination of source and destination network addresses, user makes contract for guaranteeing use of bandwidth as associated with other users located a chosen source and destination, col. 9, line 1-11 and line 33-47, col. 12, line 1-25). However, Aimoto fails to teach or suggest a single chip switch. In analogous art, Kornaros discloses (pages 30-39) a single-chip ATM switch. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to be motivated to implement a single chip switch as taught by Kornaros with the teachings of Aimoto for the purpose of minimizing the delay with respect to communicating data packets in a communication system, and at the same time miniaturizing communication device.

- 1. Claims 12-17 are allowed over prior art.
- 2. Claims 2-11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 3. Although the combined cited art teach an integrated switching system that includes switching packets/resources between devices and switching policies, they fail to teach/suggest a plurality switch ports wherein each switch port includes a port filter configured for determining a presence of a user-selected attribute in a received layer 2 data frame and outputting a signal

Application/Control Number: 09/576,021 Page 4

Art Unit: 2667

indicating the determined presence of the user-selected attribute within the data frame by one of the network switch ports having received frame, a switching module configured for identifying a presence of an output port for each data frame based on at least one of a MAC source address and MAC destination address, and notifying by the one network switch port the detected presence of the user-selected attribute to the switching module/ is absent from the art. Claims 9 and 10 depend on claim 8, therefore, claims 9 and 10 are objected to as well.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prenell P. Jones whose telephone number is 571-272-3180. The examiner can normally be reached on 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chi Pham can be reached on 571-272-3179. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Prenell P. Jone

July 12, 2005

CHI PHAM

**UPERVISORY PATENT EXAMINE** 

TECHNIOLOGY CENTER SECT